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APR 25 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No. 10/071,453
Filing Date February 8, 2002
Inventor Luan C. Tran
Assignee Micron Technology, Inc.
Group Art Unit 2813
Examiner L. M. Schillinger
Attorney Docket No. MI22-1921
Title: A Plurality of Transistors Having Different Active Area Widths and Different
Threshold Voltages Defined by STI

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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2. Response to March 23, 2005 Office Action

Dated: April 25, 2005

By: Muriel G. Dunnigan
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NUMBER OF PAGES IN FACSIMILE: 14**FEE DEFICIENCY**

Although it is believed that no fees are due, the Commissioner is hereby authorized to charge any fees under 37 C.F.R. 1.16 and 1.17 which may be required by this paper to Deposit Account No. 23-0925.

Dated: 4-25-05

By: D. Brent Kenady
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Attorney's Docket No. MI22-1921
Title: A Plurality of Transistors Having Different Active Area Widths and Different
Threshold Voltages Defined by STI (As Amended)

RESPONSE TO MARCH 23, 2005 OFFICE ACTION

To: Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

From: D. Brent Kenady
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Responsive to the Office Action dated March 23, 2005, Applicant amends
and remarks as follows:

AMENDMENTS

Underlines indicate insertions and ~~strikeouts~~ indicate deletions.